

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A driving apparatus of a plasma display panel (PDP), comprising:

a multi-chip module in which ~~at least one~~ a plurality of control chip-chips having a control circuit for controlling the PDP, and ~~at least one memory~~ a plurality of memories are mounted on a single package,

wherein the multi-chip module is mounted on a printed circuit board (PCB) of a control board, and

wherein the multi-chip module includes a plurality of green tapes, and input/output (I/O) lines coupling the ~~at least one~~ plurality of control chip-chips and the ~~at least one memory~~ plurality of memories are formed in the plurality of green tapes within the single package.

2. (Original) The driving apparatus according to claim 1, wherein the package is a ball grid type.

3. (Previously Presented) The driving apparatus according to claim 1, wherein the multi-chip module transmits a control signal to each driving unit via the PCB.

4. (Currently Amended) A driving apparatus of a plasma display panel (PDP), comprising:

a control board provided with a multi-chip module in which ~~at least one~~ a plurality of control chip-chips having a control circuit for controlling the PDP, and ~~at least one memory~~ a plurality of memories are mounted on a single package;

a plurality of driving units for generating and applying a driving signal corresponding to a control signal generated from the control board; and

a PDP for displaying an image by a plasma discharge according to the driving signal applied from each of the plurality of driving units,

wherein the multi-chip module is mounted on a printed circuit board (PCB), and input/output (I/O) lines connecting the ~~at least one~~ plurality of control chip-chips and the ~~at least one memory~~ plurality of memories are not formed directly on the PCB but are formed within the single package.

5. (Original) The driving apparatus according to claim 4, wherein the package is a ball grid type.

6. (Previously Presented) The driving apparatus according to claim 4, wherein the control board is provided with the printed circuit board (PCB) on which the single package is mounted.

7. (Currently Amended) The driving apparatus according to claim 4, wherein at least one of the control chip-chips is an ASIC type having a control circuit.

8-12. (Canceled)

13. (Currently Amended) A plasma display panel (PDP) driving apparatus comprising:  
a control board having a circuit board and a multi-chip module on the circuit board, the multi-chip module including a plurality of control chips and a plurality of memories on a single package, the control chip including a control circuit to control a PDP,

wherein the multi-chip module includes a circuit package having a plurality of circuit layers, and

wherein at least one of the plurality of control chip-chips and at least one memory of the plurality of memories are formed on a front of the circuit package and input/output (I/O) lines are formed through the plurality of circuit layers, and the I/O lines connect the at least one control chip and the at least one memory within the single package.

14. (Previously Presented) The PDP driving apparatus according to claim 13, wherein the package comprises a ball grid type.

15. (Previously Presented) The PDP driving apparatus according to claim 13, wherein the multi-chip module transmits a control signal to each driving unit via the circuit board.

16. (Previously Presented) The PDP driving apparatus according to claim 13, further comprising:

a plurality of driving units to generate and apply driving signals corresponding to control signals received from the control board.

17. (Previously Presented) The PDP driving apparatus according to claim 16, further comprising:

the PDP to display an image by a plasma discharge based on the driving signals applied from each of the plurality of driving units.

18. (Previously Presented) The PDP driving apparatus according to claim 13, wherein the control chip comprises an ASIC type.

19-22. (Canceled)